GPU QMC OPTIMIZATION

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Quantum Monte Carlo Simulation

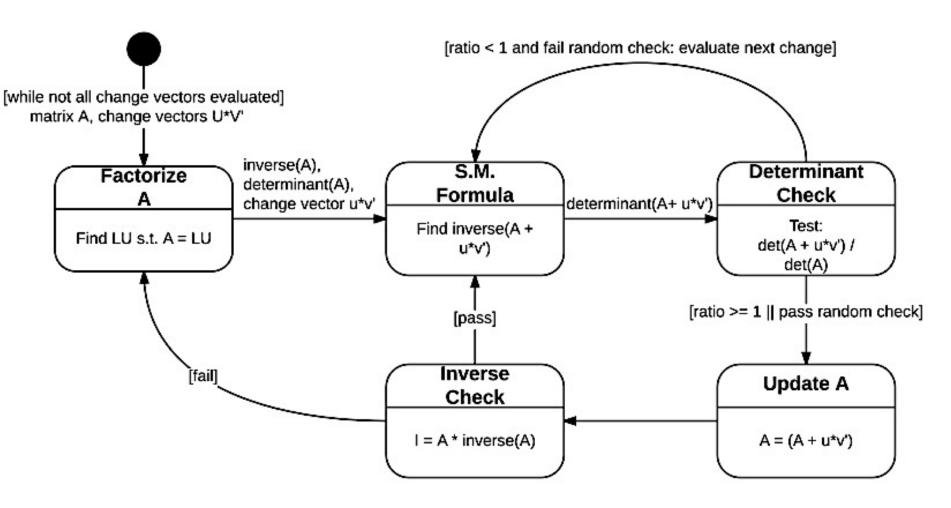
Slater Determinant for N-electrons system

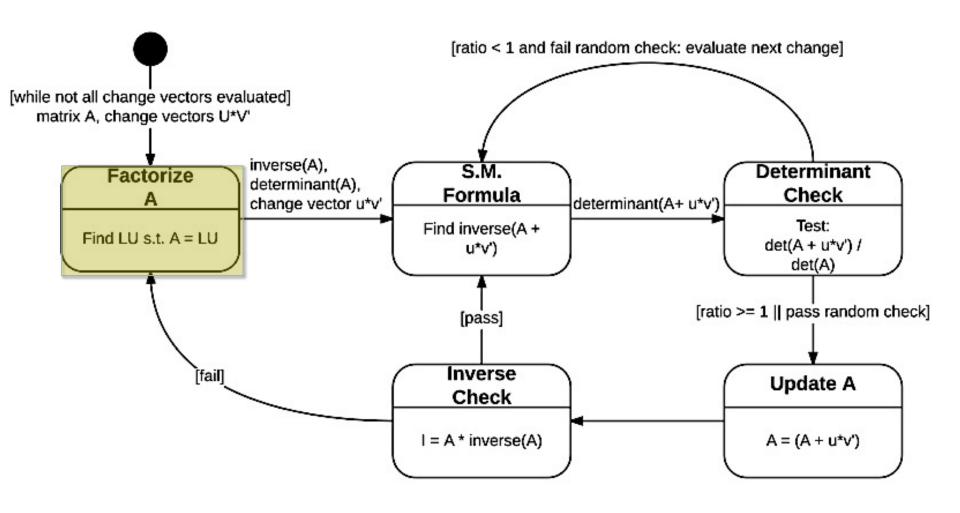
$$\Psi(\mathbf{x}_{1}, \mathbf{x}_{2}, \dots, \mathbf{x}_{N}) =$$

$$\frac{1}{\sqrt{N!}} \begin{vmatrix} \chi_{1}(\mathbf{x}_{1}) & \chi_{2}(\mathbf{x}_{1}) & \cdots & \chi_{N}(\mathbf{x}_{1}) \\ \chi_{1}(\mathbf{x}_{2}) & \chi_{2}(\mathbf{x}_{2}) & \cdots & \chi_{N}(\mathbf{x}_{2}) \\ \vdots & \vdots & \ddots & \vdots \\ \chi_{1}(\mathbf{x}_{N}) & \chi_{2}(\mathbf{x}_{N}) & \cdots & \chi_{N}(\mathbf{x}_{N}) \end{vmatrix}$$

What is QMCPACK?

- Software framework for quantum Monte Carlo simulation
- Written in C++ w/Cuda kernels
- Utilizes Cuda (acceleration) and openMP (parallelization)

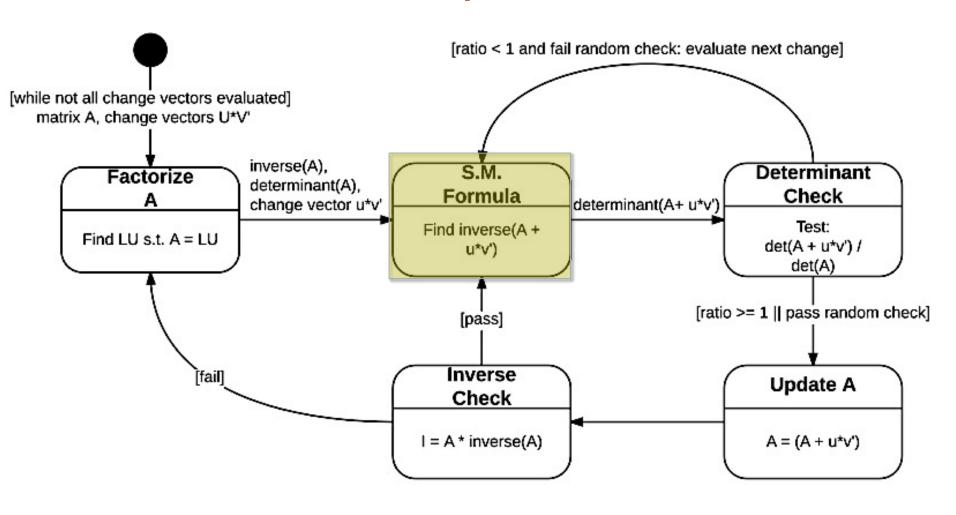


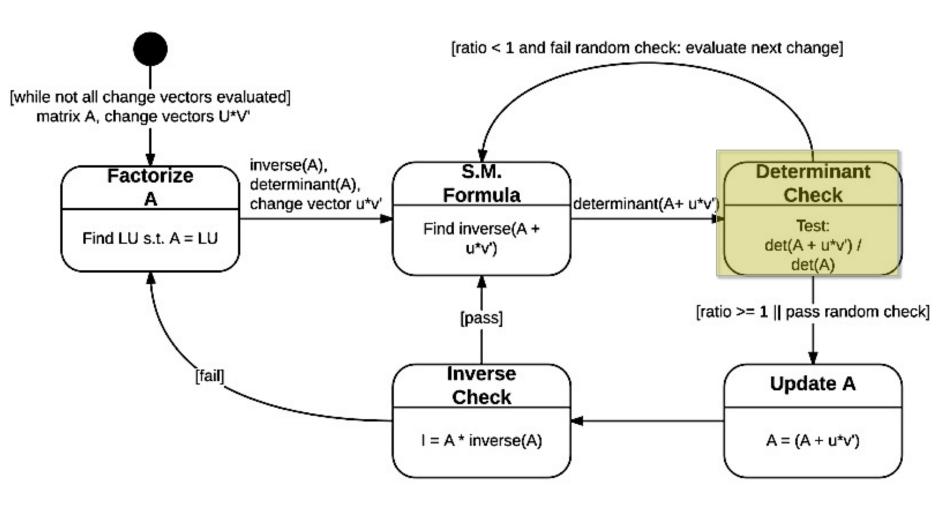


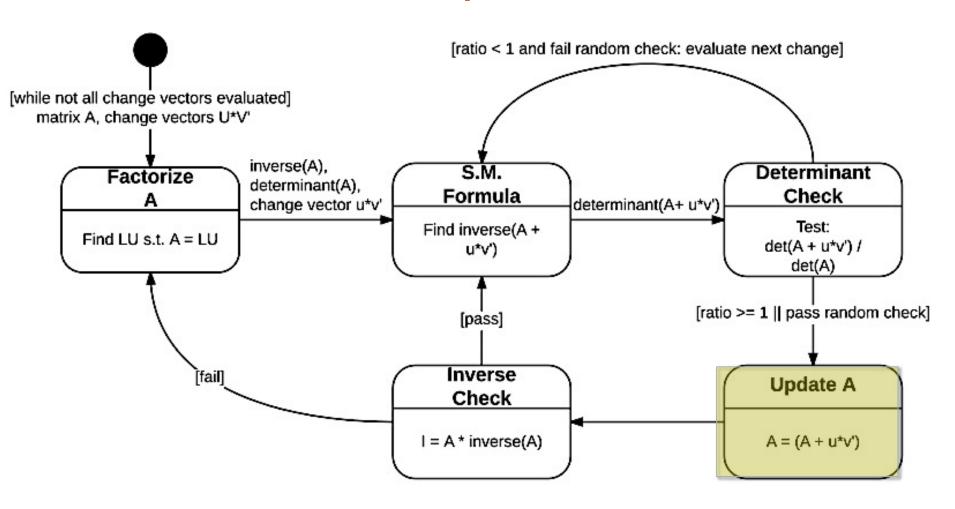
LU Decomposition

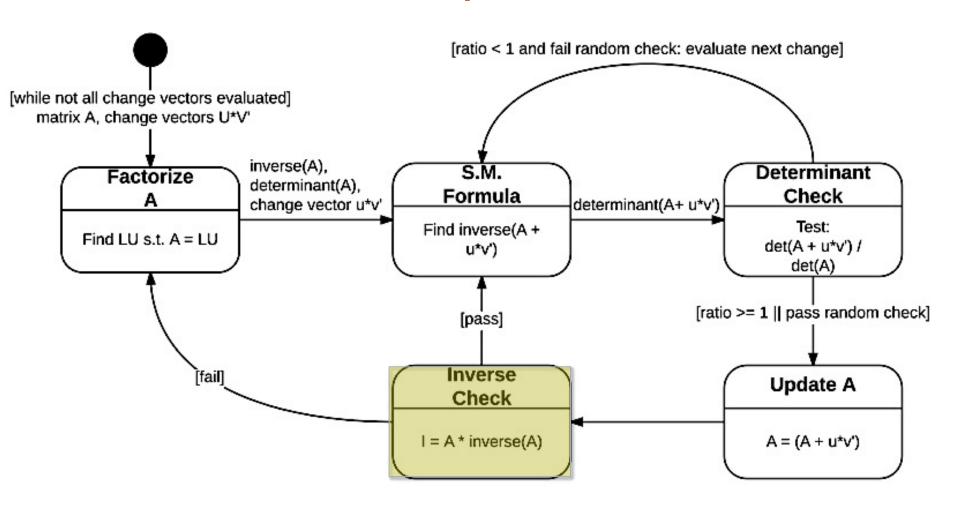
$$\begin{bmatrix} a_{11} & a_{12} & a_{13} \\ a_{21} & a_{22} & a_{23} \\ a_{31} & a_{32} & a_{33} \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ L_{21} & 1 & 0 \\ L_{31} & L_{32} & 1 \end{bmatrix} \begin{bmatrix} U_{11} & U_{12} & U_{13} \\ 0 & U_{22} & U_{23} \\ 0 & 0 & U_{33} \end{bmatrix}$$

$$A = L * U$$

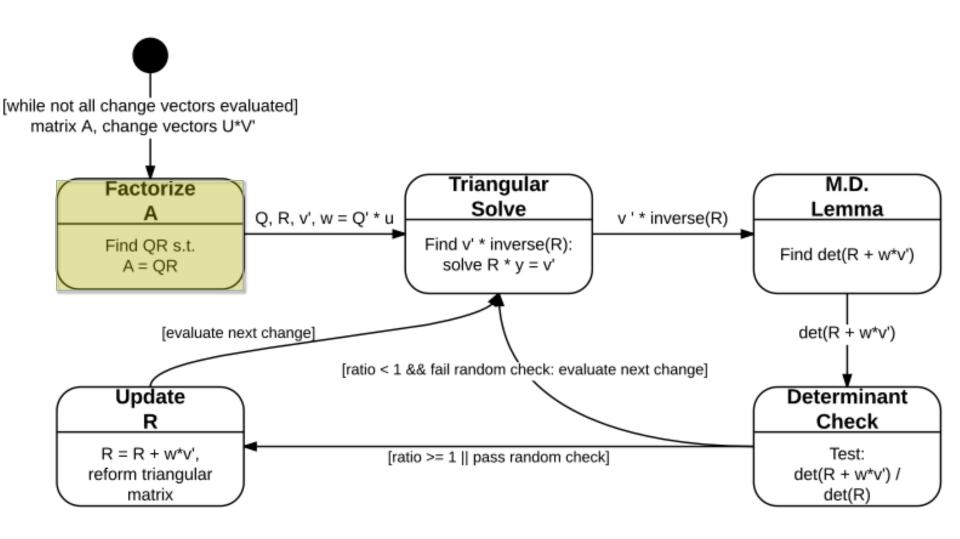








- Using QR factorization
- Rank-k update
- Triangular solve

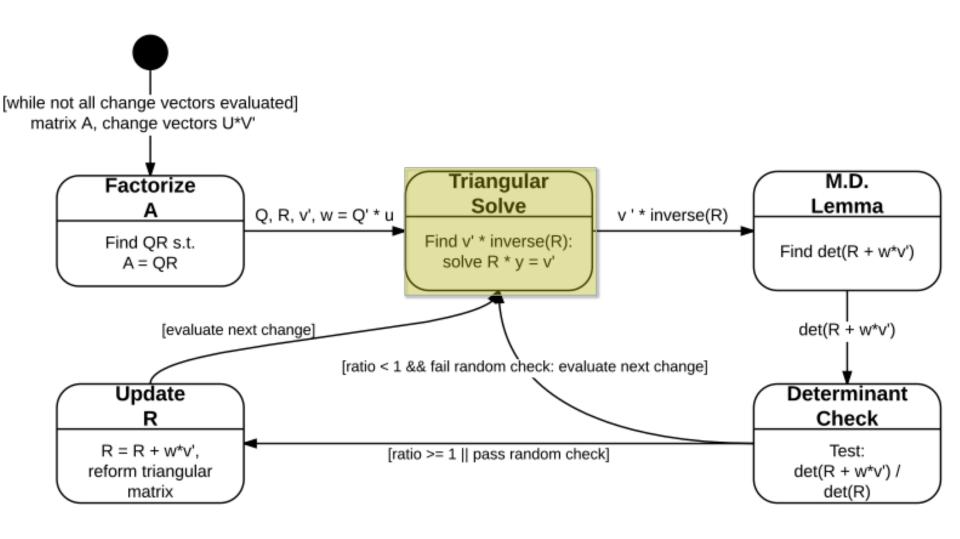


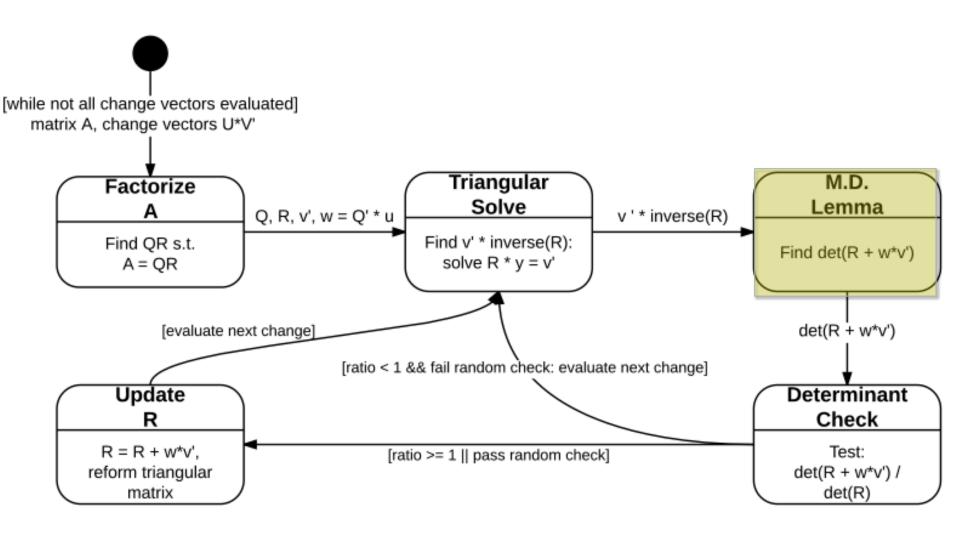
QR Decomposition

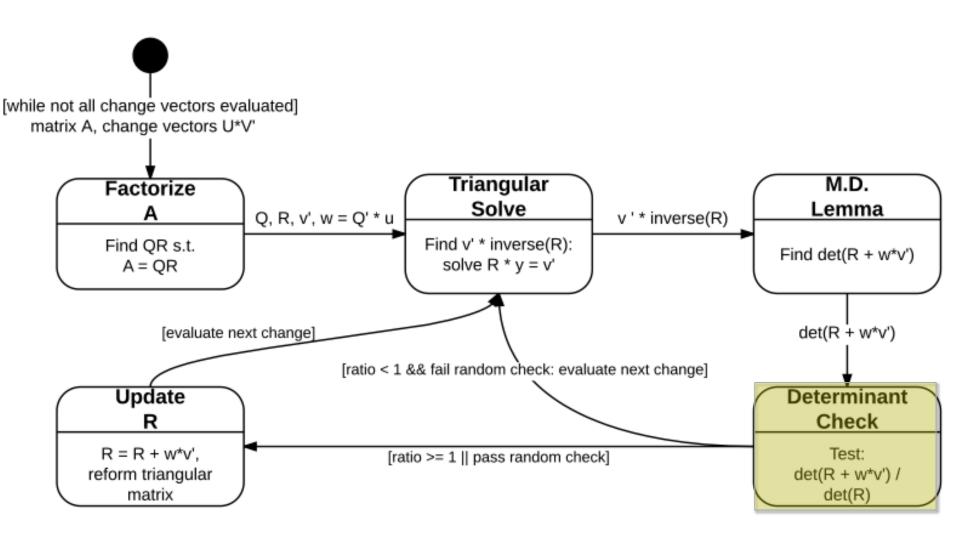
$$A = QR$$

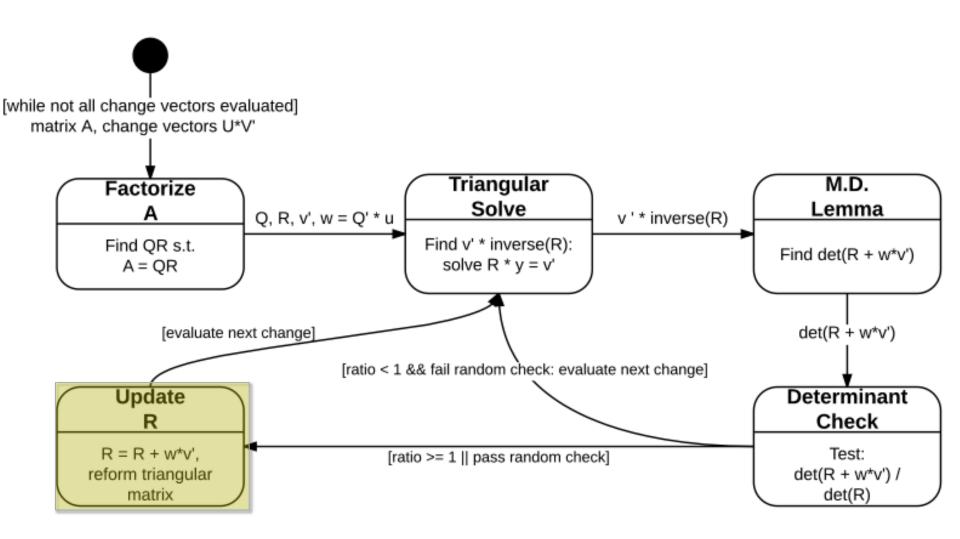
$$\left(\begin{array}{cccc} q_1 & \dots & q_n \end{array}\right) \left(\begin{array}{cccc} r_{11} & r_{12} & \dots & r_{1n} \\ 0 & r_{22} & & r_{2n} \\ \vdots & & \ddots & \vdots \\ 0 & & 0 & r_{nn} \end{array}\right)$$

Note that Q is orthonormal and R is upper triangular.









Given's Rotation

$$\left[egin{array}{cc} c & -s \ s & c \end{array}
ight]^T \left[egin{array}{cc} a \ b \end{array}
ight] = \left[egin{array}{cc} r \ 0 \end{array}
ight], \quad r = \sqrt{a^2 + b^2}.$$

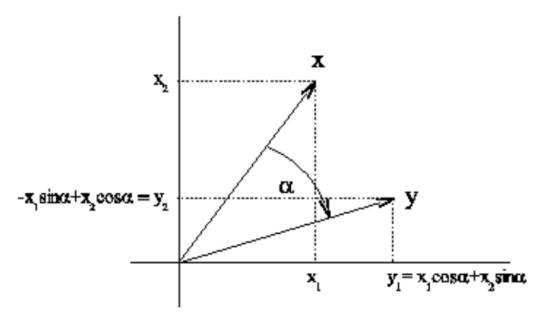
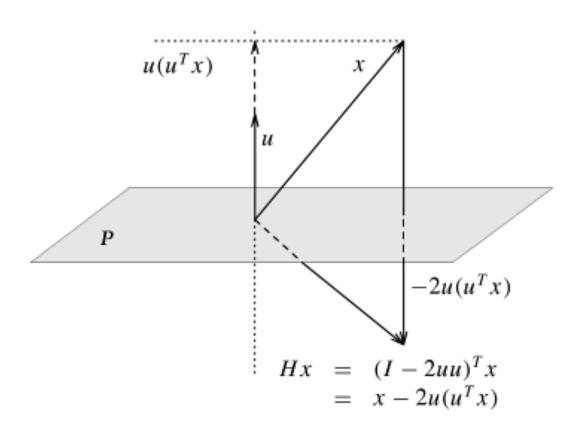


Figure 4.3: Rotation of \boldsymbol{x} in a plane by an angle α

Householder Reflection



Completed Work

- Design our algorithms, background work, version control repository
- Serial MATLAB implementations for rank-1 and rank-k updates
- Serial C/LAPACK/BLAS for rank-1 update
- cuBLAS for rank-1 update

In Progress

- Serial C/LAPACK/BLAS for rank k
- C++/Cuda kernels for rank-1/rank k update

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